## Listing of Claims

The below listing of claims will replace all prior versions of claims in the application.

Claims 1-13: Cancelled,

14. (Previously Presented) An imaging system, comprising: an image sensor, comprising:

> a sensor array, including a two-dimensional array of pixel elements, that outputs digital signals as pixel data representing an image of a scene;

> a data memory coupled to said sensor array and fabricated with said sensor array on a first integrated circuit, said data memory for storing said pixel data, wherein said data memory is configured using a first memory interface protocol;

a logic circuit coupled to said data memory and fabricated with said data memory on said first integrated circuit, said logic circuit for accessing said data memory using said first memory interface protocol and performing memory interface conversion to provide an output memory interface configured using a second memory interface protocol different than said first memory interface protocol for exporting said pixel data out of said first integrated circuit; and

an image processing device formed on a second integrated circuit separate from said first integrated circuit and including a memory interface port configured using said second memory interface protocol;

wherein said image sensor is coupled to said memory interface port of said image processing device and said image processing device accesses pixel data in said image sensor using said second memory interface protocol.

- 15. (Previously Presented) The imaging system of claim 14, wherein said second memory interface protocol of said image sensor is a SRAM interface protocol.
- 16. (Previously Presented) The imaging system of claim 14, wherein said second memory interface protocol of said image sensor is a DRAM interface protocol.

17. (Previously Presented) The imaging system of claim 14, wherein said second memory interface protocol of said image sensor is a packet protocol synchronous DRAM interface protocol.

Claims 18-21: Cancelled.